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Complete if Known Substitute for form 1449A/PTO 10/618,316 4779 Application / Conf. No. July 11, 2003 INFORMATION DISCLOSURE Filing Date Patrick Lysaght **First Named Inventor** STATEMENT BY APPLICANT **Art Unit** 2825 Vuthe Siek **Examiner Name** (use as many sheets as necessary) **Attorney Docket Number** X-1410 US of Sheet

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		OTHER NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No <sup>-1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Τ²
νŞ	•	Monica Alderighi et al.; "A Fault-Tolerant FPGA-Based Multi-Stage Interconnection Network for Space Applications"; published Computer Society; pp. 1-5.	
v'S		Ian Kyles; "Creating Large Switch Fabrics using the Three-Stage (Clos) Architecture"; Copyright VITESSE Semiconductor Corporation; pp. 1-12.	
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